

030104 17157 U.S. PTO

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Patent Application of: Leonard Forbes et al.

Title: STATIC PASS TRANSISTOR LOGIC WITH TRANSISTORS WITH MULTIPLE VERTICAL GATES

Attorney Docket No.: 303.689US2

Customer No.: 21186

PATENT APPLICATION TRANSMITTAL

MAIL STOP PATENT APPLICATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items and information (as indicated with an "X"):

- ☒ Return postcard.
 - ☒ **DIVISIONAL** of prior Patent Application No. (09/580901) (under 37 CFR 1.53(b)) comprising:
 - ☒ Specification (44 pgs, including claims numbered 1 through 60 and a 1 page Abstract).
 - ☒ Formal Drawing(s) (14 sheets).
 - ☒ Signed Declaration (6 pgs).
 - ☒ Copy of Power of Attorney from prior application (1 pgs).
 - ☒ Incorporation by Reference: *The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied herewith, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.*
 - ☒ Prior application is assigned of record to Micron Technology, Inc.
 - ☒ Information Disclosure Statement (2 pgs), Form 1449 (3 pgs)
- Citations NOT enclosed, cited in prior application.

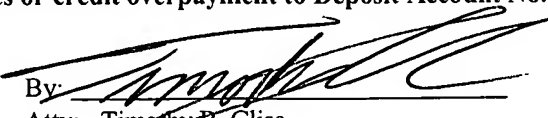
16235 U.S. PTO
10/790510

The filing fee has been calculated below as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	60-20	40	x 18.00 =	\$720.00
INDEPENDENT CLAIMS	8-3	5	x 86.00 =	\$430.00
[] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
BASIC FEE				\$770.00
TOTAL				\$1920.00

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Date of Deposit: March 1, 2004

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to The Commissioner for Patents, Mail Stop Patent Application, P.O. Box 1450, Alexandria, VA 22313-1450.